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Atty. Docket No. Q65940 PATENT APPLICATION

RESPONSE UNDER 37 C.F.R. § 1.111 U.S. Application No. 09/938,586

## **REMARKS**

Claims 1 - 5 are presently pending.

I. Rejection of Claims 1 – 5 Under 35 U.S.C. § 103 in View of Applicant's Specification and JP Publication No. 11-355056 to Matsuda

The Examiner compares certain features of the Matsuda reference to Applicant's invention, and states that a combination of Matsuda with Applicant's discussion of the prior art renders Applicant's invention obvious. This rejection is respectfully traversed as follows.

Applicant's claims 1 and 4 recite first and second amplification means and first and second amplifiers, respectively. Claims 1 and 4 also recite combining the phase-adjusted signal [of the second amplifier] with the output signal from the first amplifier. These features are altogether absent in the prior art cited by the Examiner, as explained below.

A. The Prior Art Fails to Teach or Suggest All of the Features of Applicant's Claims

While Applicant's Fig. 2 does show first and second amplifiers 11 and 12, nowhere within Applicant's discussion of the prior art (including Fig. 2) is there suggestion or disclosure of combining a phase-adjusted signal from a second amplifier with the output of a first amplifier. Fig. 2 does show the outputs of amplifiers 11 and 12 being delivered to diplexer 15; however, those of skill in the art know that a diplexer does NOT combine signals, but merely allows dual transmission of different frequencies on a singular antenna. Therefore, Applicant's Fig. 2 and related text is absolutely deficient in teaching or suggesting combining a phase-adjusted signal from a second amplifier with the output of a first amplifier, as recited by Applicant's claims 1 and 4.

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Moreover, the previous deficiencies stand uncorrected by the Matsuda reference.

Matsuda depicts a singular amplifier 1 which suppresses a harmonic of a singular transmitted signal (see Matsuda's Fig. 1). Nowhere within Matsuda is there disclosure or suggestion of a second amplifier, second amplifier means, or combining a phase-adjusted signal from a second amplifier with the output of a first amplifier. Matsuda merely combines a phase-adjusted signal from a first amplifier with the output of the first amplifier. In view of the previous, the Examiner is respectfully requested to reconsider and withdraw the instant rejection.

The Examiner's attention is also drawn to Applicant's specification at the paragraph bridging pages 3 and 4, wherein Applicant asserts that in an exemplary embodiment, the instant invention provides "a transmission circuit which can suppress unnecessary radiant waves without connecting low-pass filters to the output terminals of the respective amplifiers." Applicant respectfully asserts that the Examiner's suggested combination would necessarily include the low-pass filters connected to the output terminals of the respective amplifiers, as depicted in Fig. 2. Applicant's invention does away with the noted low-pass filters in the noted location, thereby preventing in-band ripples and attenuation of signals in the transmission frequency band.

## B. Dependent Claims

Dependent claims 2 - 3 and 5 are asserted as patentable at least by virtue of their respective dependencies. Dependent claims 2 and 5 are also asserted as patentable for including the recitations of "demultiplexing means" and a "demultiplexed output[,]" respectively. Notably, neither the Matsuda reference nor Applicant's Fig. 2 teach or suggest a demultiplexer. While Matsuda does show a "distributor" 2, those of skill in the art readily comprehend the differences

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between a demultiplexer and a distributor, in that the first reconverts a transmission containing several intermixed signals to their constituent signals, while the second merely sends an identical signal down different pathways at different times. The Examiner is therefore respectfully requested to reconsider and withdraw this rejection.

## II. Conclusion

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,

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CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this RESPONSE UNDER 37 C.F.R. § 1.111 is being facsimile transmitted to the U.S. Patent and Trademark Office this 18th day of November, 2004.

Thea K. Wagner